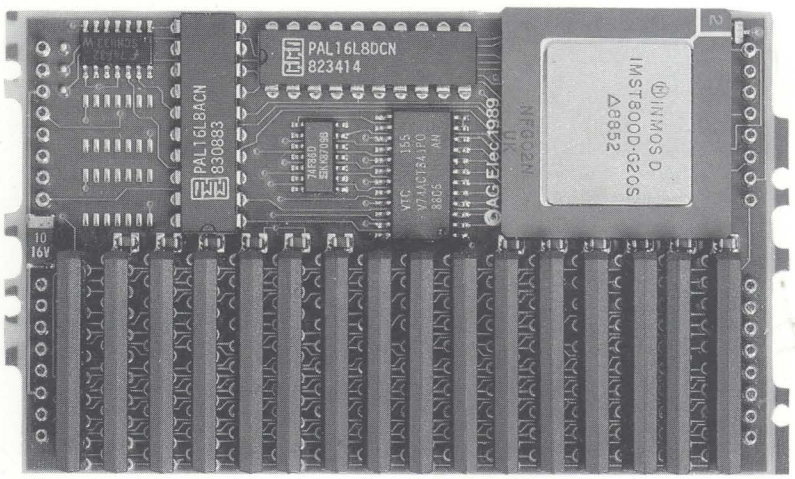
TTM

Transtech TRAMs
TTM6

TTM

Features

- **IMST800, IMST425 or IMST414 transputer options**
- **2 MBytes of dynamic RAM**
- **Zero wait state memory option**
- **20, 25 or 30 MHz transputer speed option**
- **Four serial transputer links**
- **Only 16 active pins**
- **Industry standard size 2 TRAM**
- **Compatible with Transtech range of TRAM motherboards**
- **Full Sub-system control**



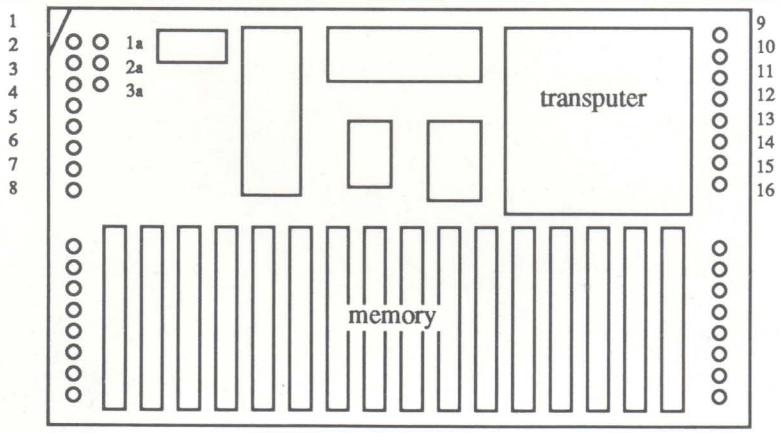
Introduction The Transtech TTM6 TRAM (TRAnsputer Module) is a small industry standard daughterboard for the Transtech range of TRAM motherboards. It has 2 MBytes of dynamic RAM and is capable of supporting the IMST800, IMST425 and IMST414 transputers. It also has the ability to control a sub-system of transputers.

TRAM Standard Measuring only 2.10" by 3.66" (5.33mm by 9.30mm) the TTM6 conforms to the published TRAM standard, allowing them to be plugged easily onto a wide range of motherboards for many different host machines. Up to 10 TRAMs can be accommodated on a Transtech TMB08 board for IBM PC XT or AT's and compatibles, 4 on the Transtech TMB04 and TMB05, 16 on a TMB12 double extended eurocard and 32 on the MCP1000 Multi Computing Platform, allowing rapid prototyping of transputer systems. Transtech TRAMs are also compatible with motherboards from other manufacturers. Further details on the TRAM standard and TRAM Module Motherboard Architecture are published by Prentice Hall in 'Transputer Technical Notes' ISBN 0-130929126-1.

Transtech TTM6

Functional Description

TRAMs use 16 pins for communication with the motherboard and for obtaining power. However, TRAMs that are larger than size 1 have more than 16 pins, with the extra pins providing more power and ground connections. The extra pins also propagate the signals from the motherboard below to allow stacking of modules. The link speed of the TRAMs is selected by two pins. When both are held low the links operate at 10 Mbits/sec and when high at 20 Mbits/sec. This is implemented by jumpers or switches on the motherboards. The allocation of the pins are shown in the following diagram.



1 Link2Out	9 Link3In
2 Link2In	10 Link3Out
3 VCC	11 GND
4 Link1Out	12 Link0In
5 Link1In	13 Link0Out
6 LinkSpeedA	14 notError
7 LinkSpeedB	15 Reset
8 ClockIn(5MHz)	16 Analyse

Ordering Information

Part Number	Processor Type	Processor Cycle Time (ns)	Memory (MBytes)	Memory CycleTime (ns)
TTM6-4	IMST414-20	50	2	200
TTM6-42	IMST425-20	50	2	200
TTM6-8	IMST800-20	50	2	200
TTM6-8-F	IMST800-20	50	2	150
TTM6-85	IMST800-25	40	2	160
TTM6-85-F	IMST800-25	40	2	120
TTM6-830	IMST800-30	33	2	132

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